

General Description

JMX65R190ME series is power MOSFET using advanced super junction technology that can realize very low on-resistance and gate charge. It will provide much high efficiency by using optimized charge coupling technology. It is engineered to minimize conduction loss, JMX65R190ME series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

Features

- ❖ Low $R_{DS(ON)}$ & FOM
- ❖ Low Power Loss by High Speed Switching and Low On-Resistance
- ❖ Excellent stability and uniformity

Applications

- | | |
|---|---|
| <ul style="list-style-type: none"> ❖ PC power ❖ LED lighting ❖ Telecom power | <ul style="list-style-type: none"> ❖ Server power ❖ EV Charger ❖ Solar/UPS |
|---|---|

ORDERING INFORMATION

ACKAGE	TEMPERATURE RANGE	ORDERING PART NUMBER	TRANSPORT MEDIA
TO-252-2L	-55 °C to 150 °C	JMD65R190METR	Tape and Reel 2500 units
TO-263-2L	-55 °C to 150 °C	JMB65R190METR	Tape and Reel 800 units
TO-220F	-55 °C to 150 °C	JMF65R190METH	Tape 1000 units
TO-220	-55 °C to 150 °C	JMA65R190METH	Tape 1000 units

Package & Pin Information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V _{DS}	650	V
Gate-source voltage	V _G S	±30	V
Continuous drain current ¹⁾ , $TC=25^\circ\text{C}$	ID	20	A
Continuous drain current ¹⁾ , $TC=100^\circ\text{C}$		10.5	
Pulsed drain current ²⁾ , $TC=25^\circ\text{C}$	ID, pulse	62	A
Continuous diode forward current ¹⁾ , $TC=25^\circ\text{C}$	I _S	20	A
Diode pulsed current ²⁾ , $TC=25^\circ\text{C}$	I _S , pulse	62	A
Power dissipation ³⁾ , $TC=25^\circ\text{C}$	PTO-252-2L	PD	W
Power dissipation ³⁾ , $TC=25^\circ\text{C}$		102	W
Power dissipation ³⁾ , $TC=25^\circ\text{C}$	TO-263-2L	PD	115
Power dissipation ³⁾ , $TC=25^\circ\text{C}$	TO-220F	PD	39
Power dissipation ³⁾ , $TC=25^\circ\text{C}$	TO-220P	PD	42
Single pulsed avalanche energy ⁵⁾	EAS	300	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\dots 480\text{ V}$	dv/dt	50	V/ns

Reverse diode dv/dt, VDS=0...480 V, ISD≤ID	dv/dt	15	V/ns
Operation and storage temperature	Tstg, T _j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	R _{θJC}	0.9	°C/W
Thermal resistance, junction-case	R _{θJC}	3.4	°C/W
Thermal resistance, junction-case	R _{θJC}	0.88	°C/W
Thermal resistance, junction-case	R _{θJC}	0.85	°C/W
Thermal resistance, junction-ambient ⁴⁾	R _{θJA}	62	°C/W

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BVDSS	650			V	VGS=0 V, ID=250 μA
Gate threshold voltage	VGS(th)	2.5		3.9	V	VDS=VGS, ID=250 μA
Drain-source on-state resistance	RDS(ON)		0.17	0.19	Ω	VGS=10 V, ID=10 A
Gate-source leakage Current	IGSS			100	nA	VGS=30 V
				-100		VGS=-30 V
Drain-source leakage Current	IDSS			1	μA	VDS=650 V, VGS=0 V

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{iss}		1280		pF	VGS=0 V, VDS=400 V, f=1M KHz
Output capacitance	C _{oss}		30		pF	
Reverse transfer capacitance	C _{rss}		9.1		pF	
Turn-on delay time	td(on)		32		ns	VGS=10 V, VDS=400 V, RG=2 Ω, ID=10 A
Rise time	tr		65		ns	
Turn-off delay time	td(off)		98		ns	
Fall time	tf		62		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		29.0		nC	VGS=10 V, VDS=400 V, ID=10 A
Gate-source charge	Q _{gs}		8.5		nC	
Gate-drain charge	Q _{gd}		10.0		nC	
Gate plateau voltage	V _{plateau}		5.2		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V _{SD}			1.4	V	IS=20 A, VGS=0 V
Reverse recovery time	t _{rr}		345		ns	VR=400 V, IS=10 A, di/dt=100 A/μs
Reverse recovery charge	Q _{rr}		8.6		μC	
Peak reverse recovery current	I _{rrm}		23.7		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R_{θJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
- 5) V_{DD}=100 V, V_{GS}=10 V, L=80 mH, starting T_j=25 °C.

Electrical Characteristics Diagrams

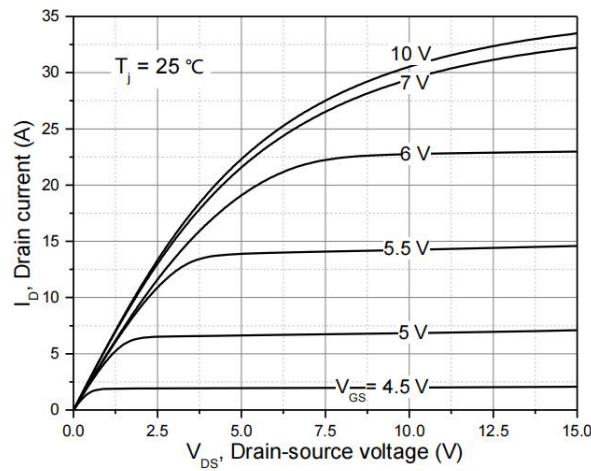


Figure 1. Typ. output characteristics

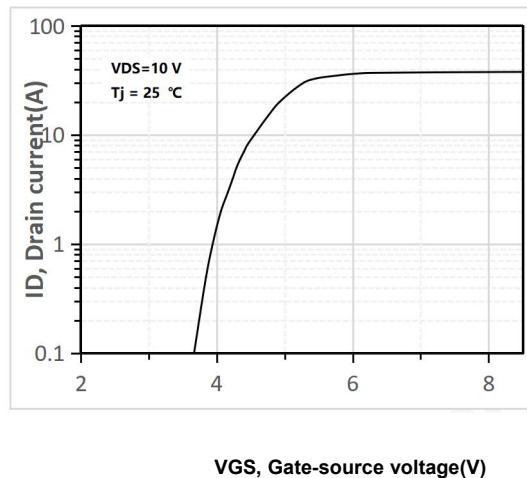


Figure 2. Typ. transfer characteristics

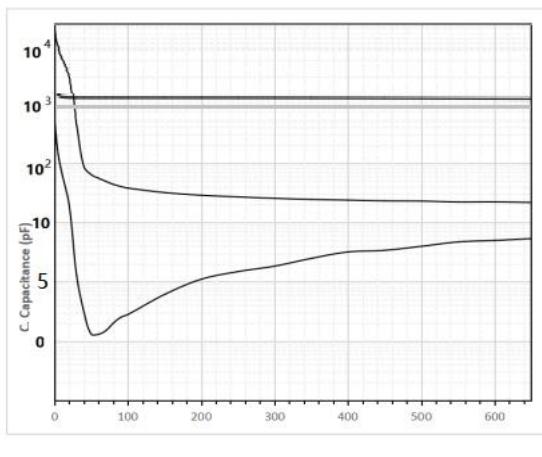


Figure 3. Typ. capacitances

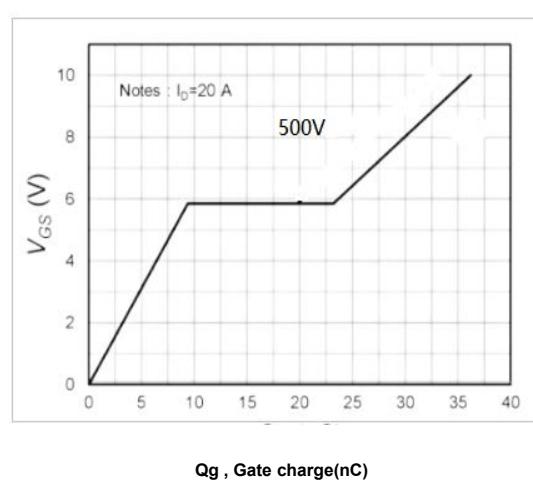
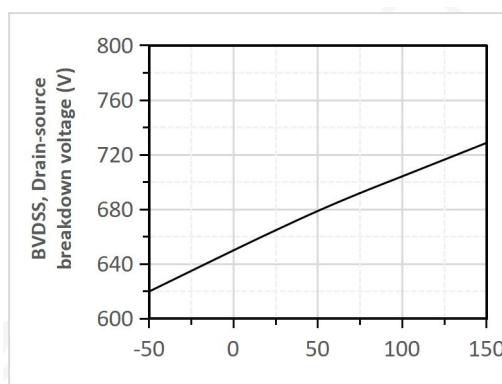
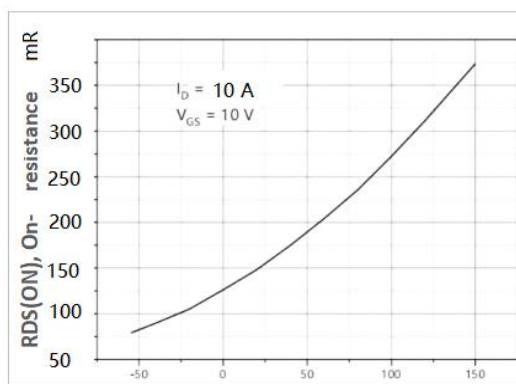


Figure 4. Typ. gate charge



T_j, Junction temperature (°C)
Figure 5. Drain-source breakdown voltage



T_j, Juntion temperature (°C)
Figure 6. Drain-source on-state resistance

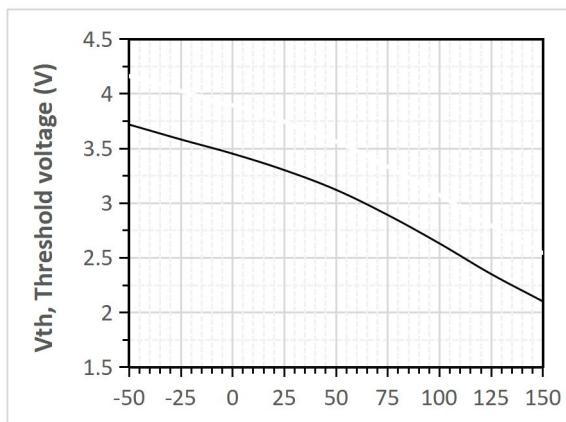


Figure 7. Threshold voltage

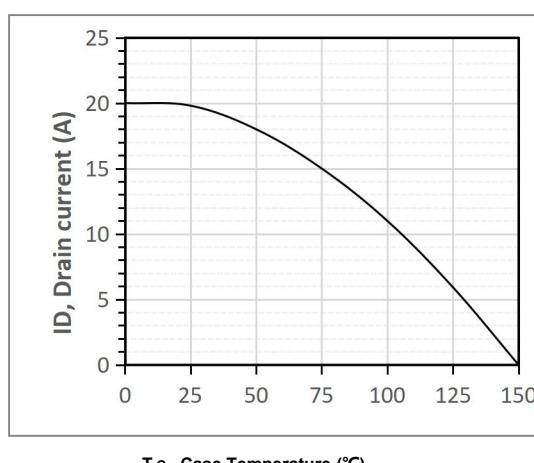


Figure 8. Drain current

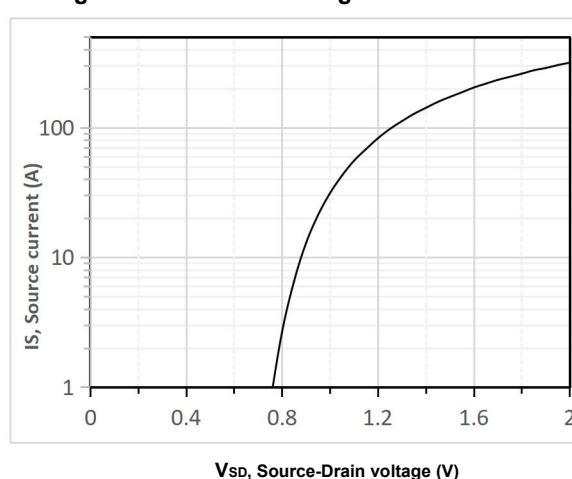


Figure 9. Forward characteristic of body diode

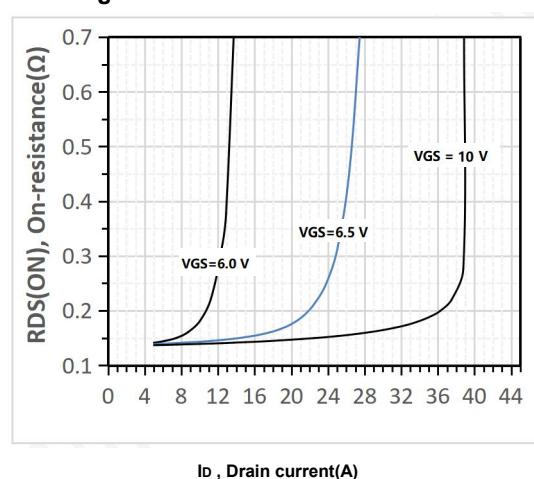


Figure 10. Drain-source on-state resistance

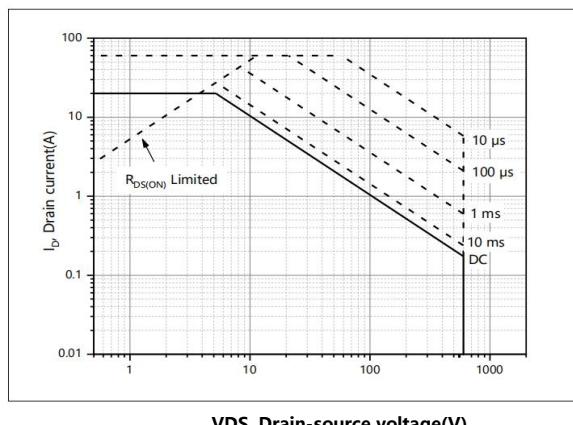


Figure 11. Safe operation area $T_c=25\text{ }^{\circ}\text{C}$

Test circuits and waveforms

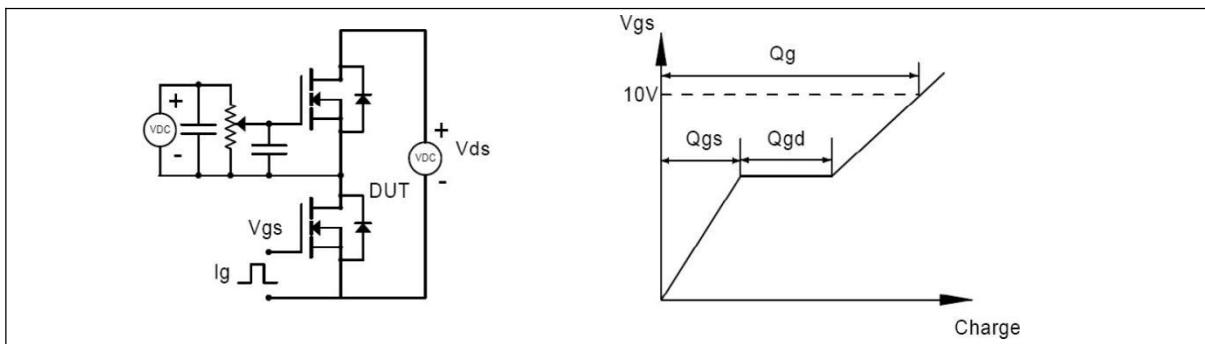


Figure 1. Gate charge test circuit & waveform

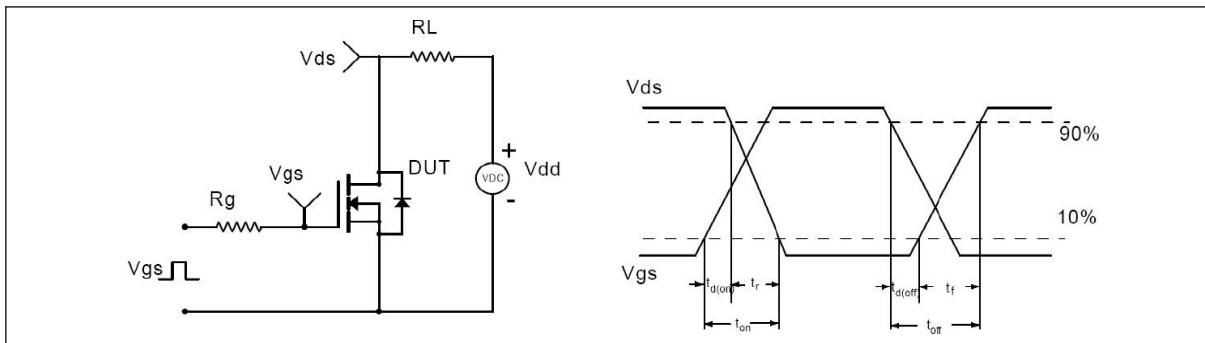


Figure 2. Switching time test circuit & waveforms

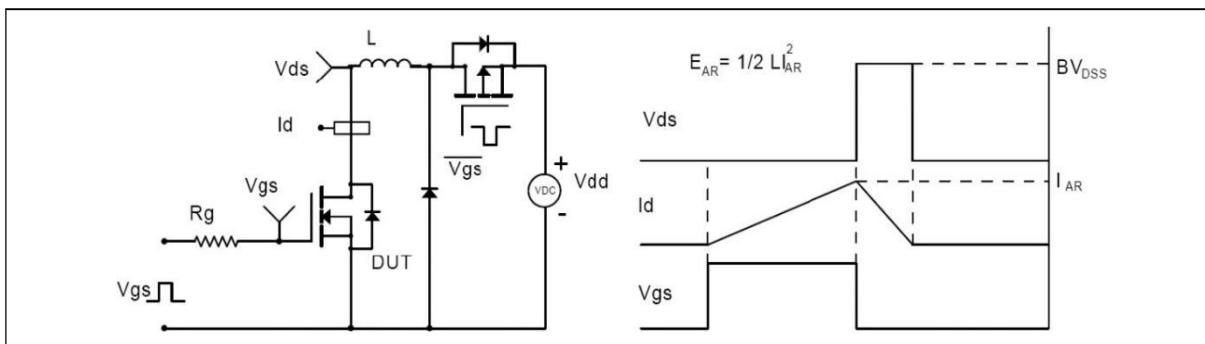


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

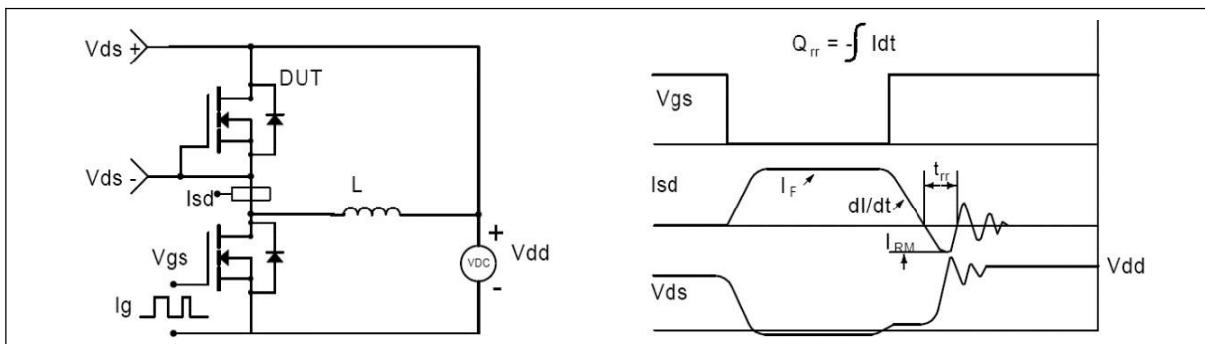
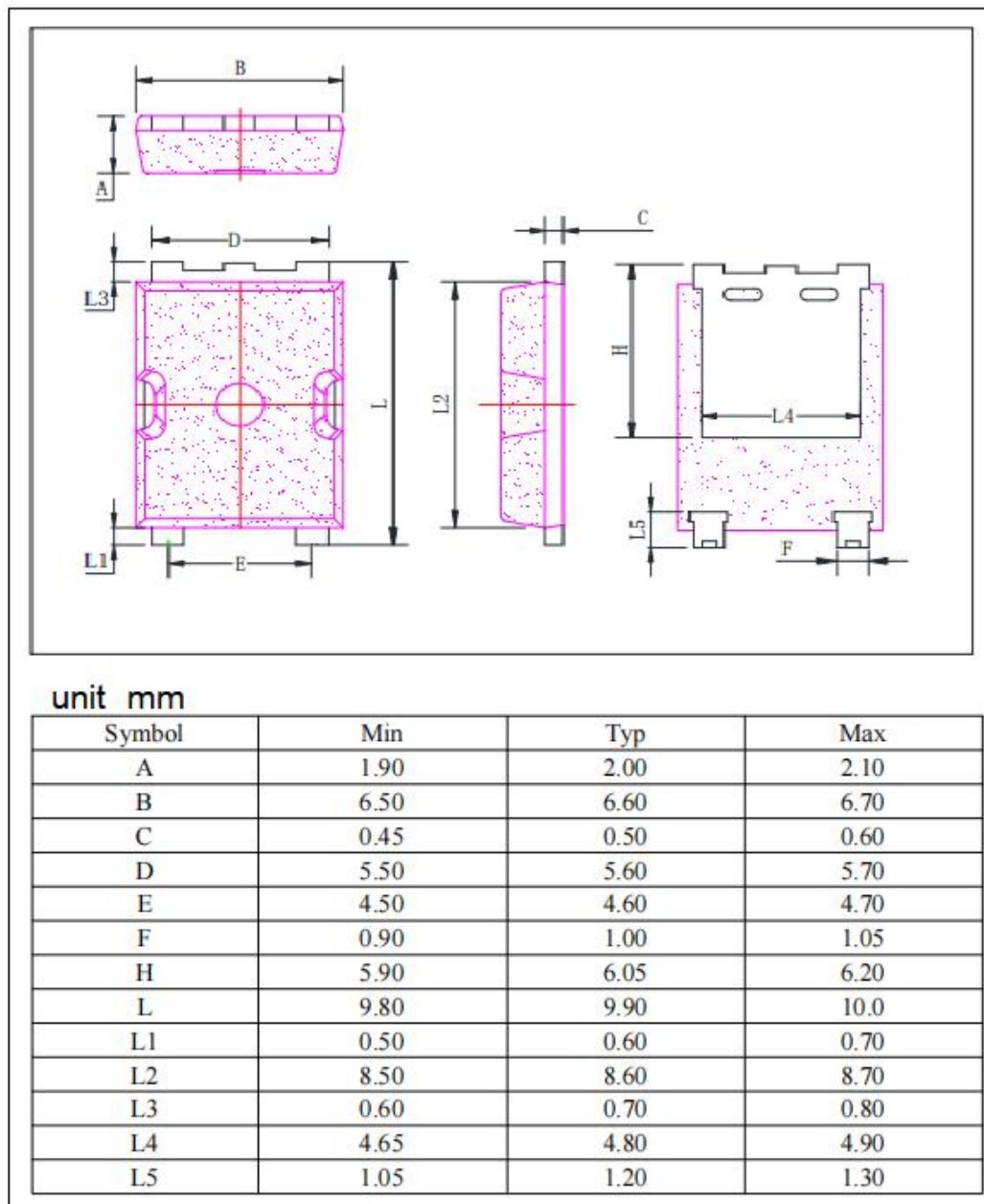
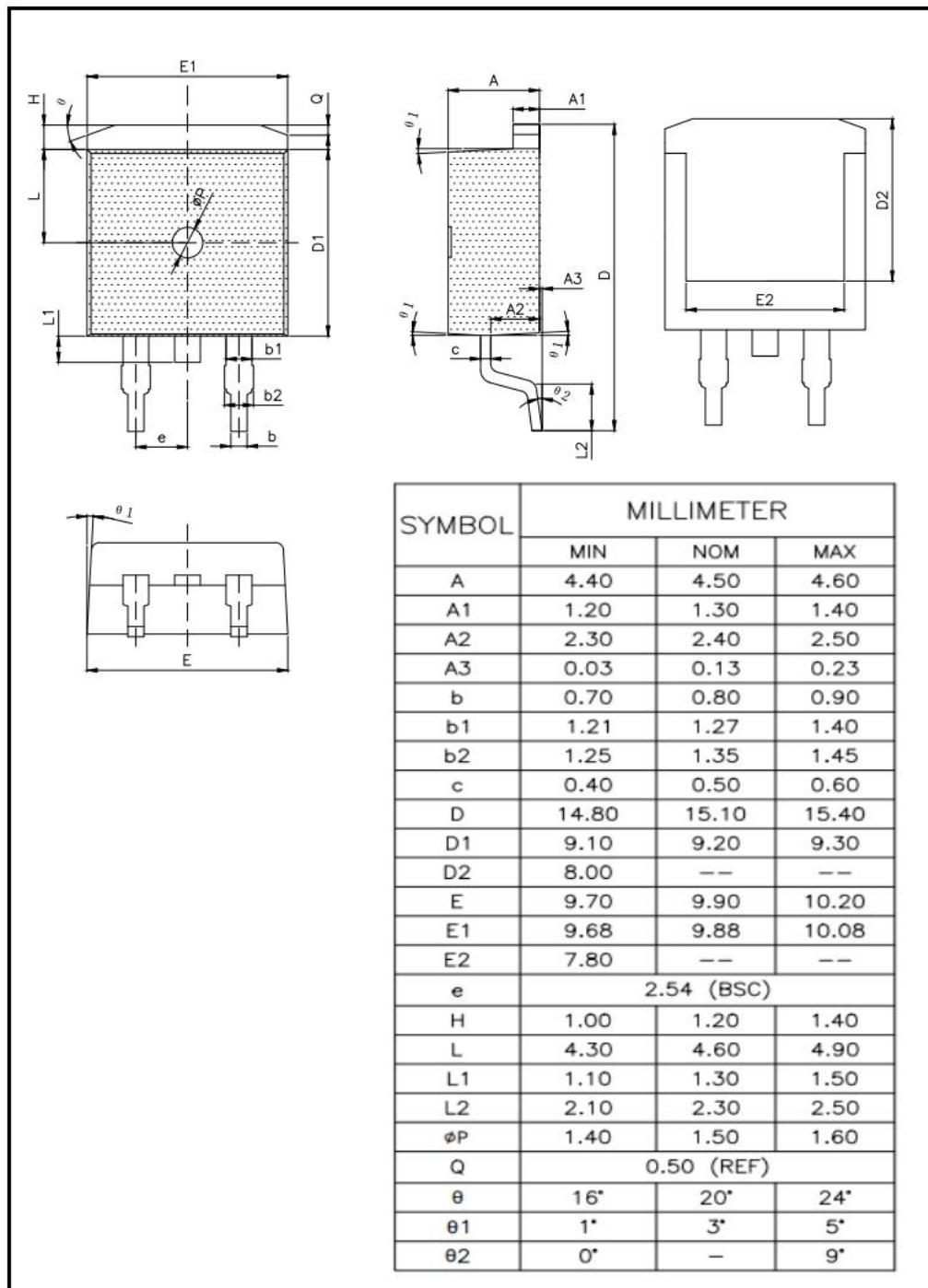


Figure 4. Diode reverse recovery test circuit & waveforms

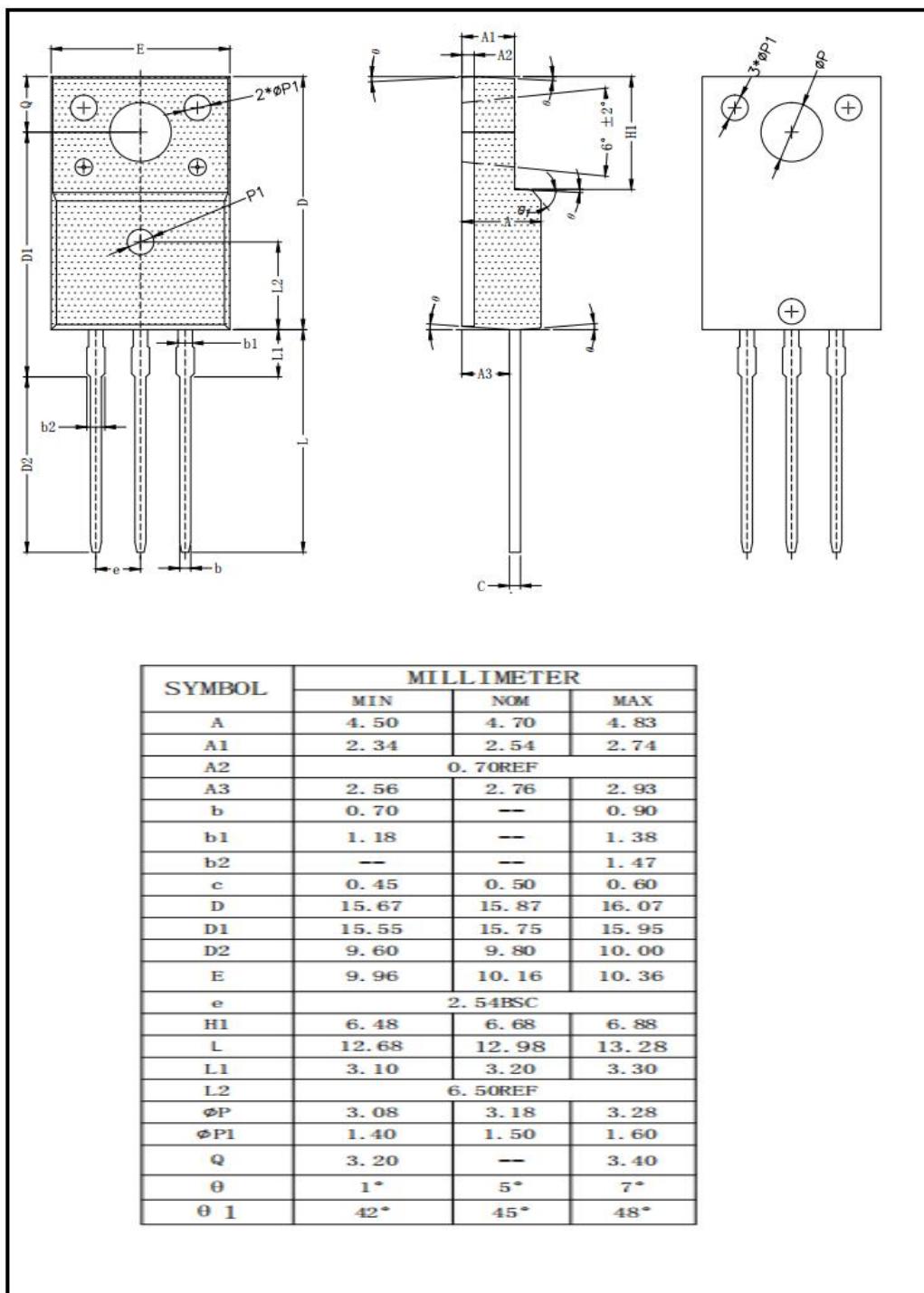
Package Information PTO252



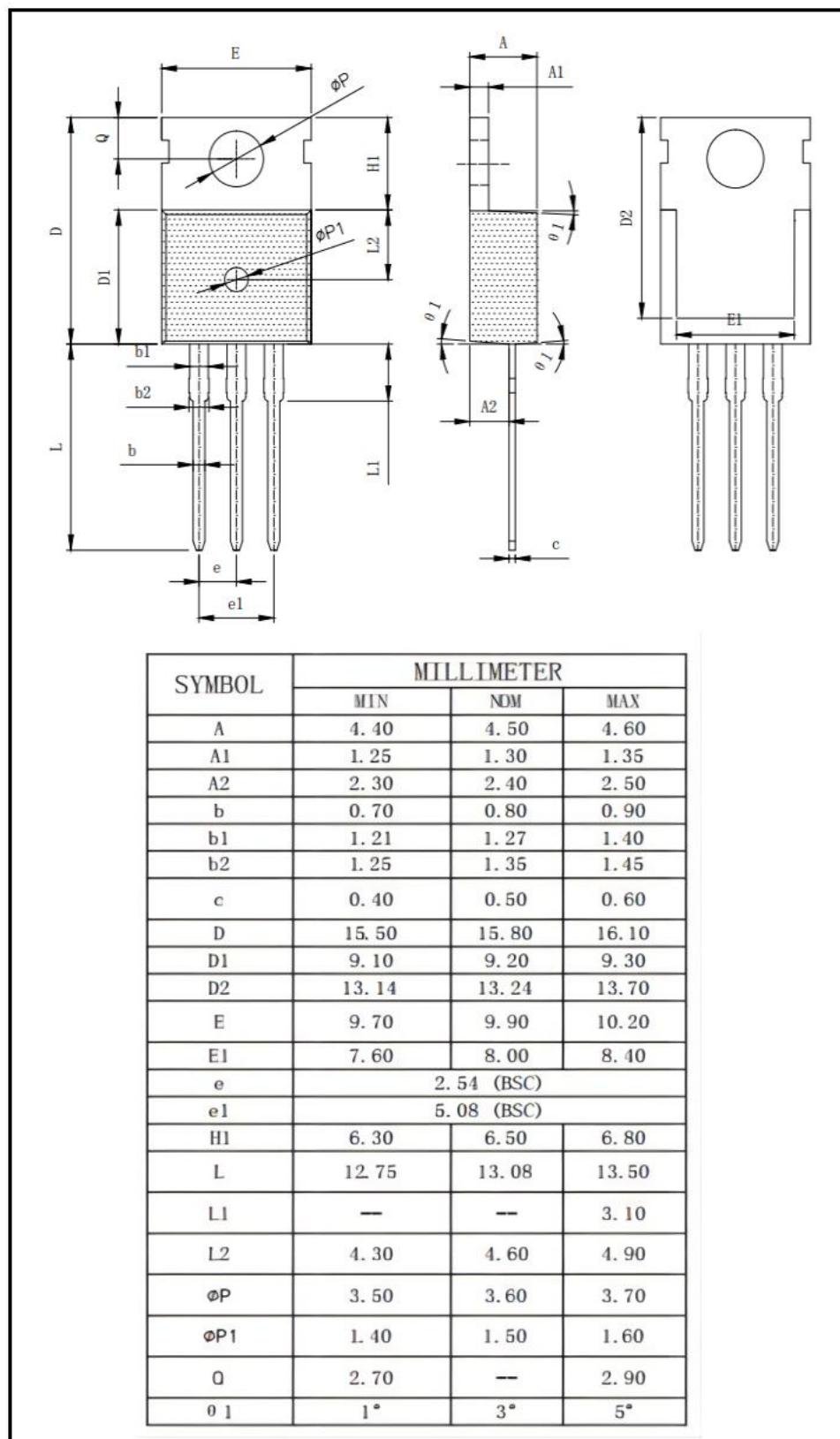
Package Information TO263-2L



Package Information TO220-F



Package Information TO220P



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