

## General Features

- Low RDS(ON)
- Low Dense Cell Design
- Reliable and Rugged
- Advanced trench process technology

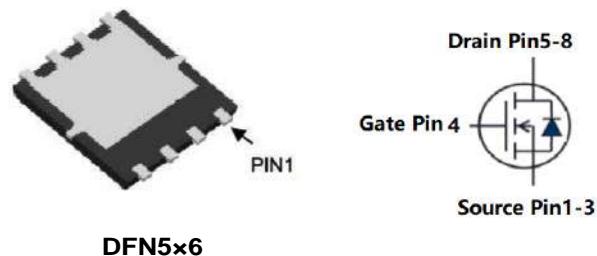
## Product Summary



$V_{DSS}$	30	V
$R_{DS(ON)-Typ}$	3.3	$m\Omega$
$I_D$	50	A

## Application

- Power Management in Inverter System
- Synchronous Rectification



## Maximum ratings, at $T_A = 25^\circ C$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{DSS}$	Drain-Source breakdown voltage	30	V
$I_s$	Diode continuous forward current	$T_c = 25^\circ C$	A
$I_D$	Continuous drain current @ $VGS=10V$	$T_c = 25^\circ C$	A
		$T_c = 100^\circ C$	A
$I_{DM}$	Pulse drain current tested ①	$T_c = 25^\circ C$	A
EAS	Avalanche energy, single pulsed ②	250	mJ
$P_D$	Maximum power dissipation	$T_c = 25^\circ C$	W
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$T_{STG}, T_J$	Storage and Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	4.5	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	35	$^\circ C/W$

## Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ <math>T_j=25^\circ\text{C}</math> (unless otherwise stated)</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_\text{D}=250\mu\text{A}$	30	--	--	V
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}$	--	--	1	$\mu\text{A}$
	Zero Gate Voltage Drain Current( $T_j=125^\circ\text{C}$ )	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}$	--	--	100	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Body Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	--	--	$\pm 100$	nA
$\text{V}_{\text{GS(TH)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_\text{D}=250\mu\text{A}$	1.0	1.5	2.5	V
$\text{R}_{\text{DS(ON)}}$	Drain-Source On-State Resistance <sup>③</sup>	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_\text{D}=20\text{A}$	--	3.3	4.5	$\text{m}\Omega$
$\text{R}_{\text{DS(ON)}}$	Drain-Source On-State Resistance <sup>③</sup>	$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_\text{D}=10\text{A}$	--	5.5	8	$\text{m}\Omega$
<b>Dynamic Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{V}_{\text{DS}}=15\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{f}=1\text{MHz}$	--	860	--	pF
$\text{C}_{\text{oss}}$	Output Capacitance		--	140	--	pF
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		--	105	--	pF
$\text{R}_{\text{d}}$	Gate Resistance	$\text{f}=1\text{MHz}$	--	2.7	--	$\Omega$
$\text{Q}_{\text{g}}(10\text{V})$	Total Gate Charge	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_\text{D}=25\text{A}, \text{V}_{\text{GS}}=10\text{V}$	--	19	--	nC
$\text{Q}_{\text{g}}(4.5\text{V})$	Total Gate Charge		--	13	--	nC
$\text{Q}_{\text{gs}}$	Gate-Source Charge		--	4.3	--	nC
$\text{Q}_{\text{gd}}$	Gate-Drain Charge		--	6.5	--	nC
<b>Switching Characteristics</b>						
$t_{\text{d(on)}}$	Turn-on Delay Time	$\text{V}_{\text{DD}}=15\text{V}, \text{I}_\text{D}=25\text{A}, \text{R}_\text{G}=3.0\Omega, \text{V}_{\text{GS}}=10\text{V}$	--	6	--	ns
$t_r$	Turn-on Rise Time		--	5	--	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	25	--	ns
$t_f$	Turn-Off Fall Time		--	7	--	ns
<b>Source- Drain Diode Characteristics@ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$\text{V}_{\text{SD}}$	Forward on voltage	$\text{I}_{\text{SD}}=25\text{A}, \text{V}_{\text{GS}}=0\text{V}$	--	0.9	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$\text{T}_j=25^\circ\text{C}, \text{I}_{\text{SD}}=25\text{A}, \text{V}_{\text{GS}}=0\text{V}$ $d\text{I}/dt=500\text{A}/\mu\text{s}$	--	7	--	ns
$\text{Q}_{\text{rr}}$	Reverse Recovery Charge		--	6.3	--	nC

NOTE:

① Repetitive rating; pulse width limited by max junction temperature.

② Limited by  $\text{T}_{\text{Jmax}}$ , starting  $\text{T}_j = 25^\circ\text{C}$ ,  $L = 0.5\text{mH}$ ,  $\text{R}_G = 25\Omega$ ,  $\text{I}_{\text{AS}} = 9\text{A}$ ,  $\text{V}_{\text{GS}} = 10\text{V}$ . Part not recommended for use above this value

③ Pulse width  $\leq 300\mu\text{s}$ ; duty cycle  $\leq 2\%$ .

### Typical Characteristics

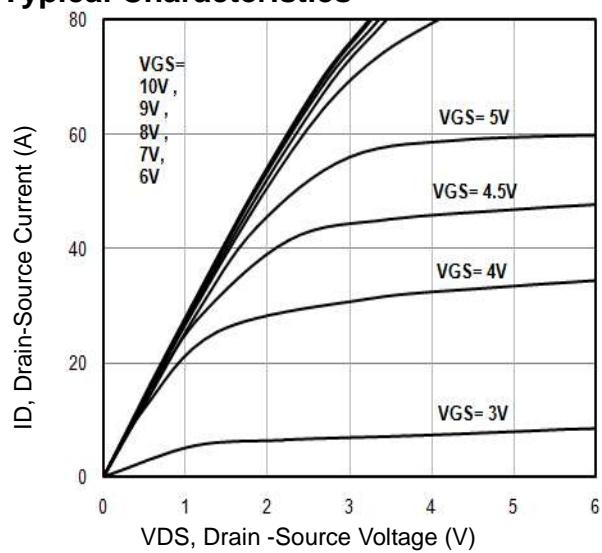


Fig1. Typical Output Characteristics

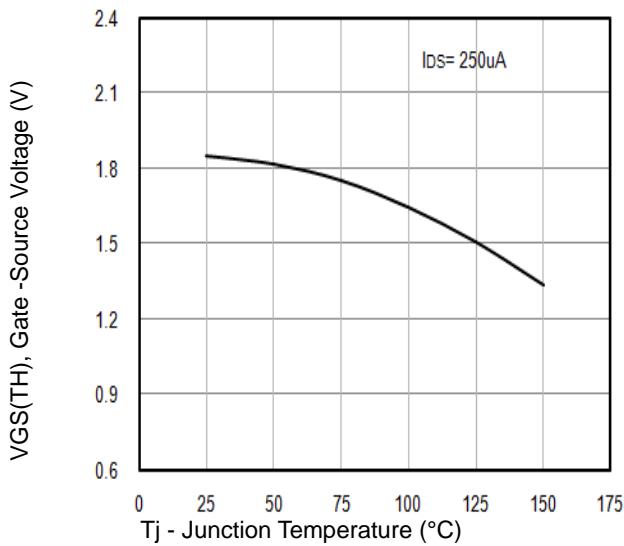


Fig2.  $V_{GS(TH)}$  Gate -Source Voltage Vs.  $T_j$

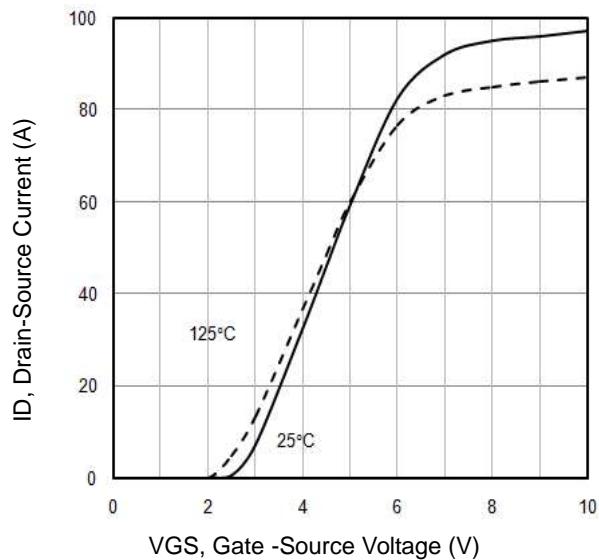


Fig3. Typical Transfer Characteristics

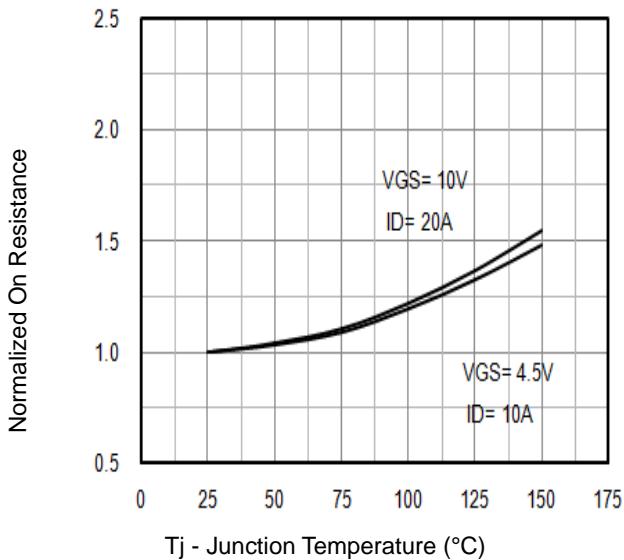


Fig4. Normalized On-Resistance Vs.  $T_j$

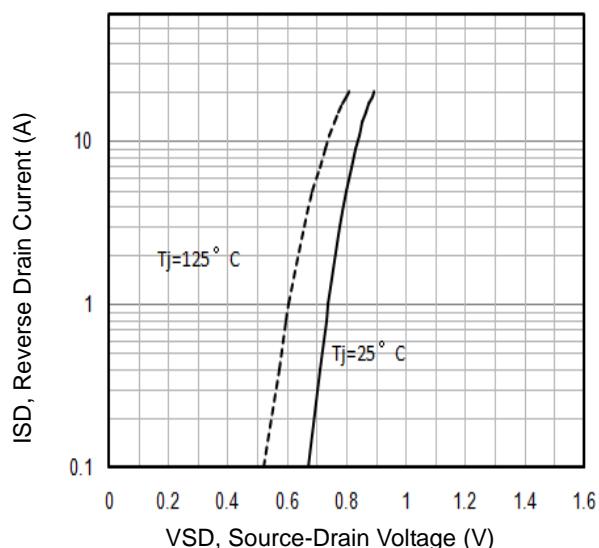


Fig5. Typical Source-Drain Diode Forward Voltage

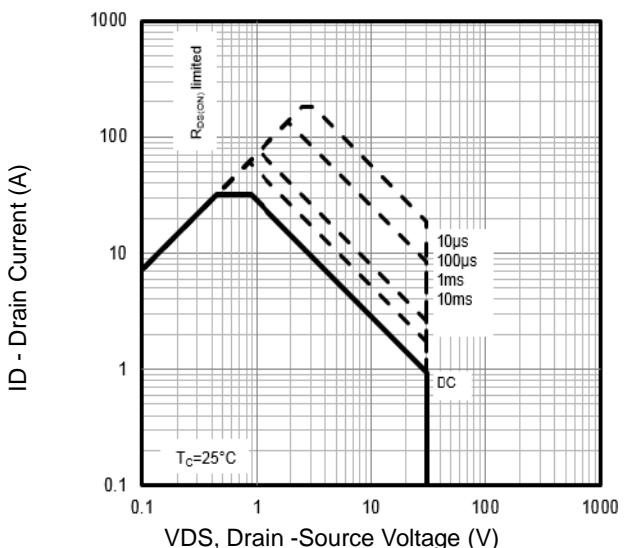


Fig6. Maximum Safe Operating Area

## Typical Characteristics

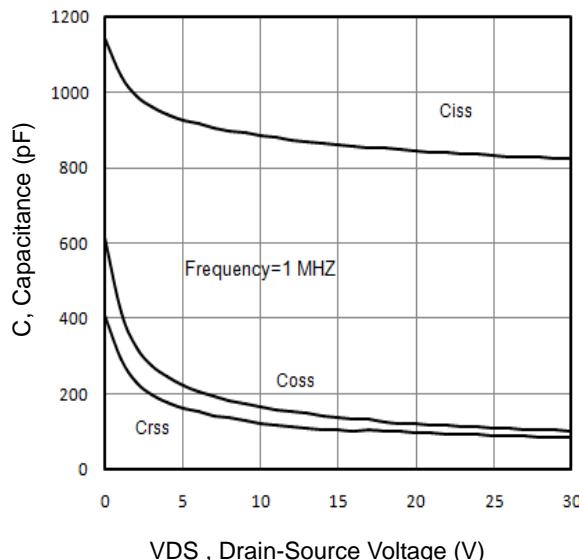


Fig7. Typical Capacitance Vs.Drain-Source Voltage

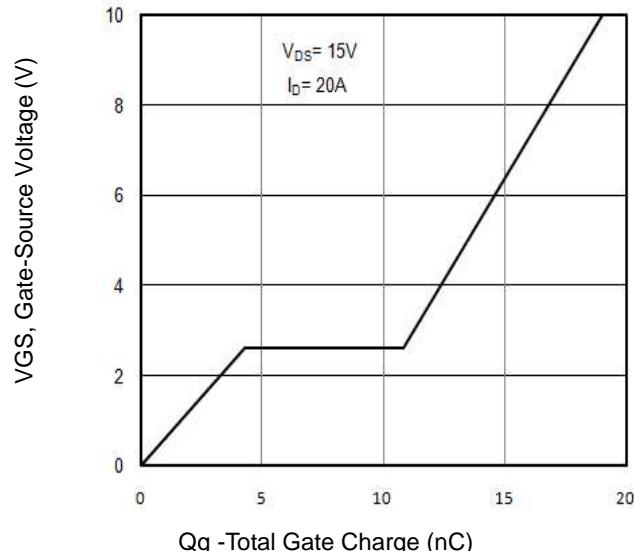


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

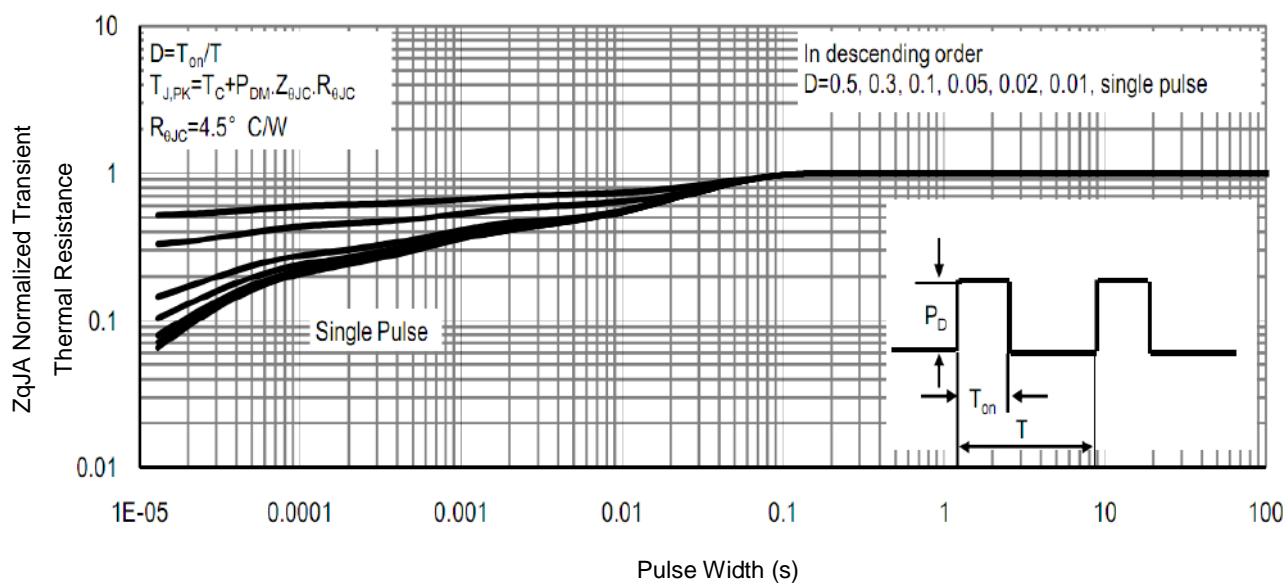


Fig9. Normalized Maximum Transient Thermal Impedance

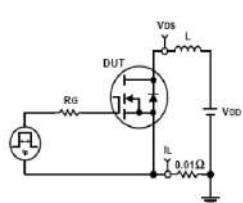


Fig10. Unclamped Inductive Test Circuit and waveforms

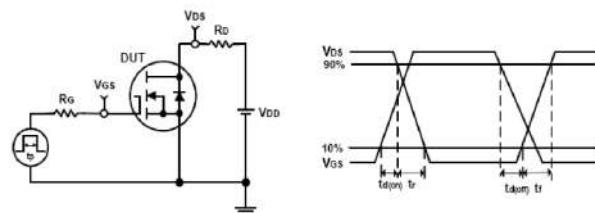
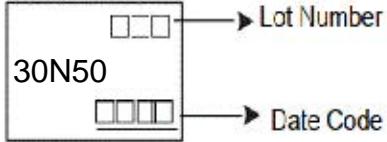


Fig11. Switching Time Test Circuit and waveforms

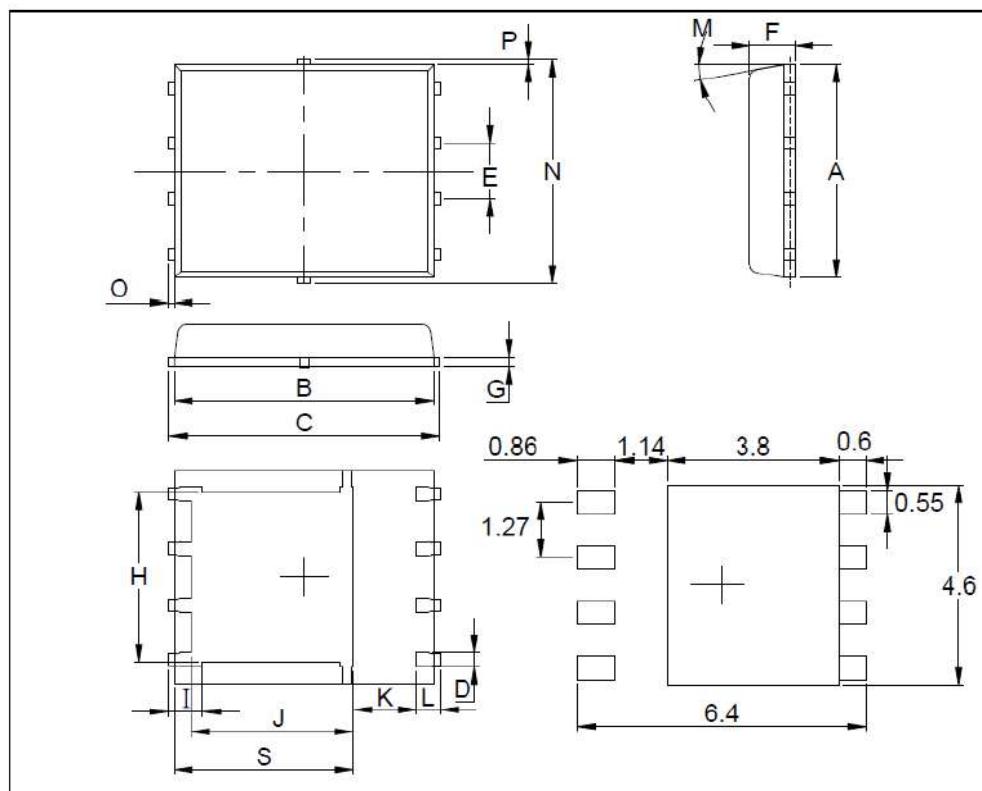
## Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
JMN30N50Q-R	30N50	DFN5x6-8	Tape&Reel	5000

PACKAGE	MARKING
DFN5*6-8	 <p>The marking diagram shows a rectangular label with the text "30N50" in the center. Above the "30N50" text, there are three small squares representing a lot number, with an arrow pointing to it labeled "Lot Number". Below the "30N50" text, there are four small squares representing a date code, with an arrow pointing to it labeled "Date Code".</p>

### PDFN 5x6P MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8		5.15	J	3.33		3.78
B	5.44		5.9	K	0.9		
C	5.9		6.35	L	0.35		0.712
D	0.33		0.51	M	0°		12°
E		1.27		N	4.8		5.5
F	0.8		1.25	O	0.05		0.3
G	0.15		0.34	P	0.06		0.2
H	3.61		4.31	S	3.69		4.19
I	0.35		0.71				



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