

Features

- $V_{DS} = 20V$ $I_D = 7.0A$
- Low gate charge
- Built-in G-S protection diode against ESD
- ESD Rating :2000V HBM
- Lead Free Available (RoHS Compliant)

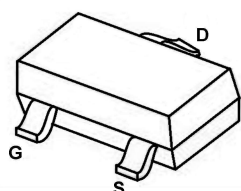
Applications

- Load Switch
- PWM Application

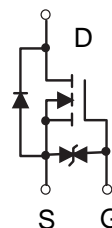
Product Summary



V_{DS}	20	V
$R_{DS(on),MAX} @ V_{GS}=4.5V$	18	mΩ
I_D	7.0	A



SOT-23



N Channel MOSFET

Ordering Information

Ordering Device No.	Package	Packing	Quantity
JME3416EZA	SOT-23	Tape&Reel	3000/Reel

Absolute Maximum Ratings ($T_A=25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit	
V_{DS}	Drain-Source Voltage	20	V	
V_{GSS}	Gate-Source Voltage	± 12		
I_D^*	Continuous Drain Current	7.0	A	
I_{DM}^*	300μs Pulsed Drain Current			28
I_S^*	Diode Continuous Forward Current	7.0	A	
T_J	Maximum Junction Temperature	150	$^\circ C$	
T_{STG}	Storage Temperature Range	-55 to 150		
P_D^*	Maximum Power Dissipation	$T_A=25^\circ C$	1.4	W
		$T_A=100^\circ C$	1.1	
$R_{\theta JA}^*$	Thermal Resistance-Junction to Ambient	89	$^\circ C/W$	

Notes :

*Surface Mounted on 1in² pad area, $t \leq 10sec.$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Condition	LIMITS			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	20			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=20V, V_{GS}=0V$ $T_J=85^{\circ}\text{C}$			1	μA
					10	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	0.5	0.7	0.9	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$			± 9	μA
$R_{DS(ON)}^a$	Drain-Source On-state Resistance	$V_{GS}=4.5V, I_{DS}=4A$		13	18	$m\Omega$
		$V_{GS}=2.5V, I_{DS}=3A$		17	22	
Diode Characteristics						
t_{rr}	Reverse Recovery Time	$I_{DS}=7.0A, di_{SD}/dt=100A/\mu s$		15		ns
Q_{rr}	Reverse Recovery Charge			8		nC

Electrical Characteristics (Cont.) ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Condition	LIMITS			Unit
			Min.	Typ.	Max.	
Dynamic Characteristics^b						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$		3.5		Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=10V,$ Frequency=1.0MHz		655		pF
C_{oss}	Output Capacitance			150		
C_{rss}	Reverse Transfer Capacitance			85		
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=10V, R_L=12.5\Omega,$ $I_{DS}=6.5A, V_{GEN}=5V,$ $R_G=25\Omega$		1.9		ns
T_r	Turn-on Rise Time			1.3		
$t_{d(OFF)}$	Turn-off Delay Time			13		
T_f	Turn-off Fall Time			5		
Gate Charge Characteristics^b						
Q_g	Total Gate Charge	$I_{DS}=6.5A, di_{SD}/dt=100A/\mu s$		7.9		nC
Q_{gs}	Gate-Source Charge			2.4		
Q_{gd}	Gate-Drain Charge			2.9		

Notes :

- a : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- b : Guaranteed by design, not subject to production testing.

Typical Electrical and Thermal Characteristics

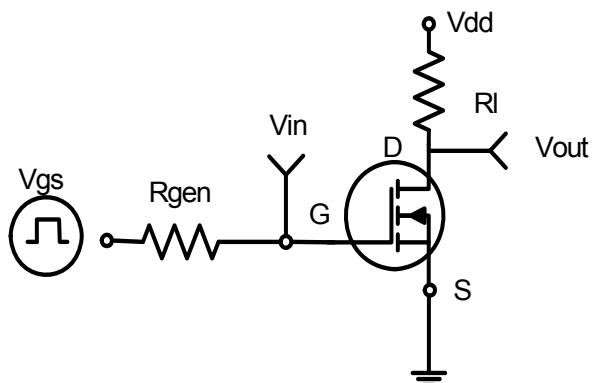


Figure 1: Switching Test Circuit

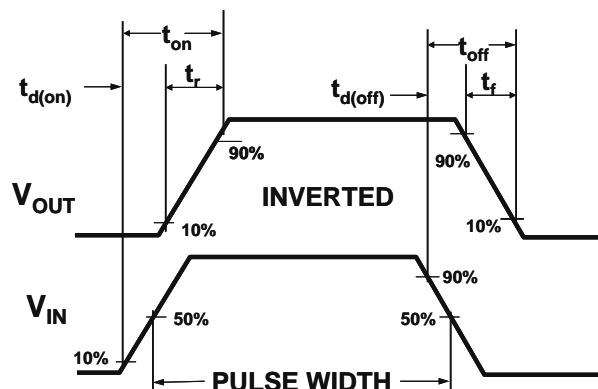


Figure 2: Switching Waveforms

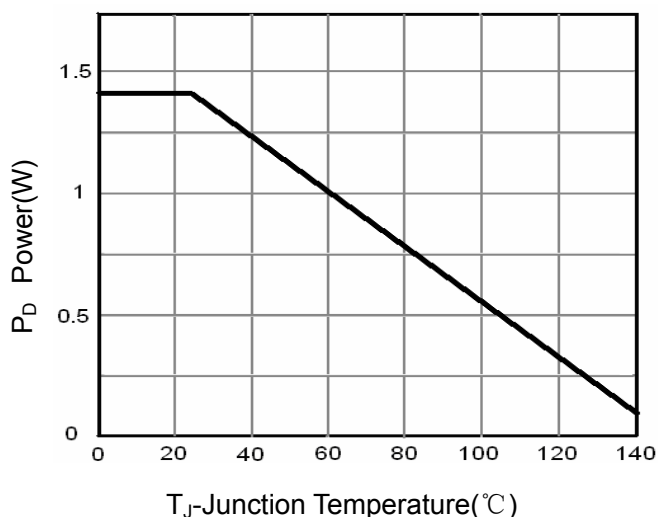


Figure 3 Power Dissipation

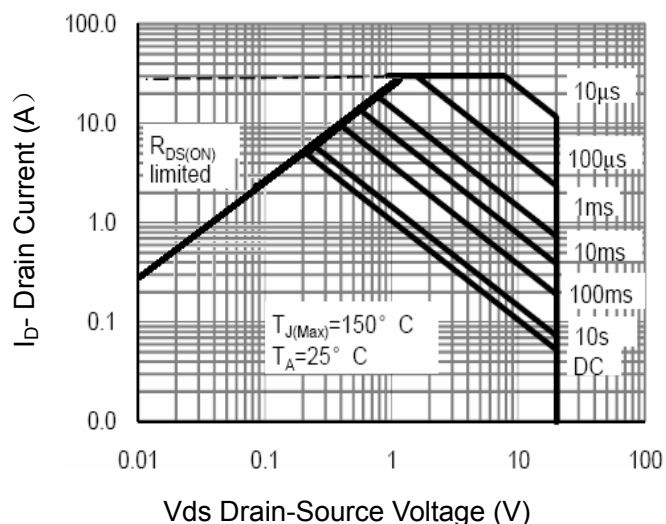


Figure 4 Safe Operation Area

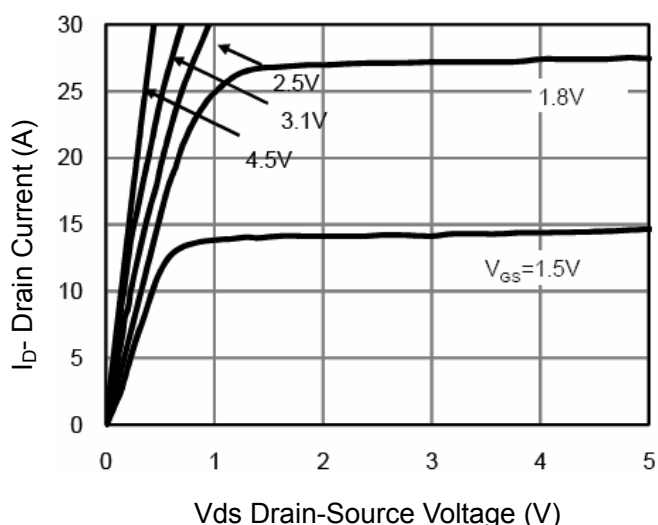


Figure 5 Output Characteristics

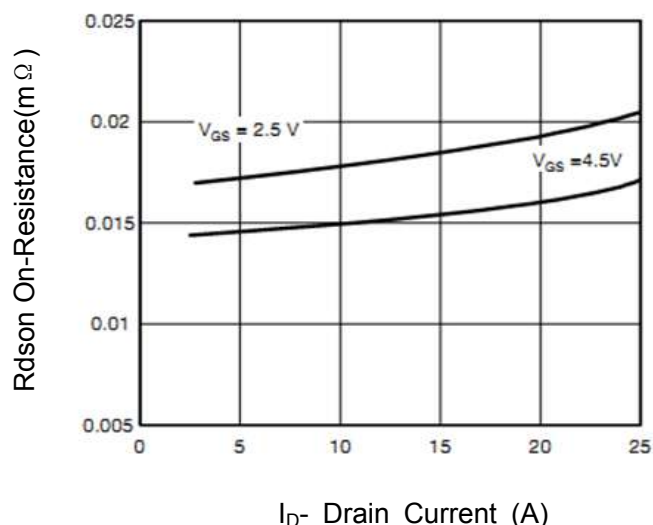


Figure 6 Drain-Source On-Resistance

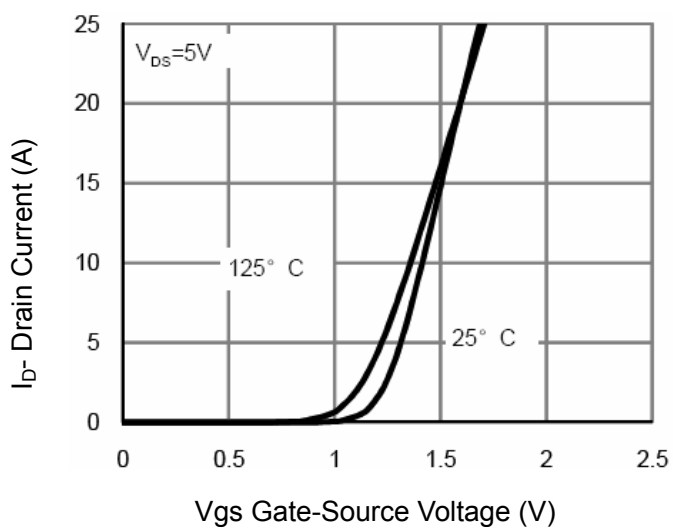


Figure 7 Transfer Characteristics

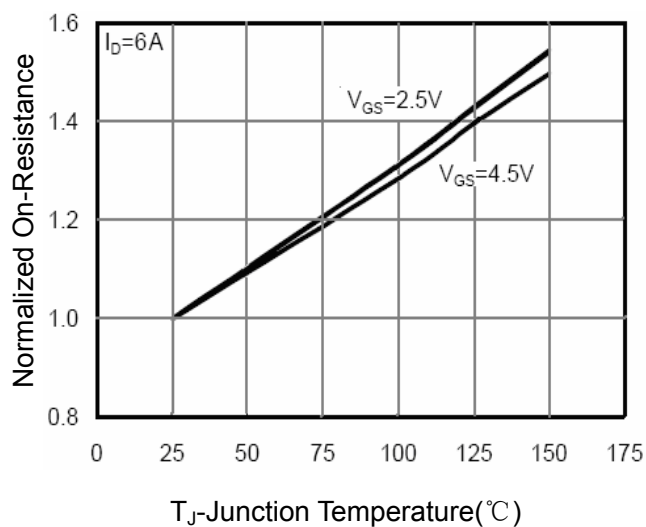


Figure 8 Drain-Source On-Resistance

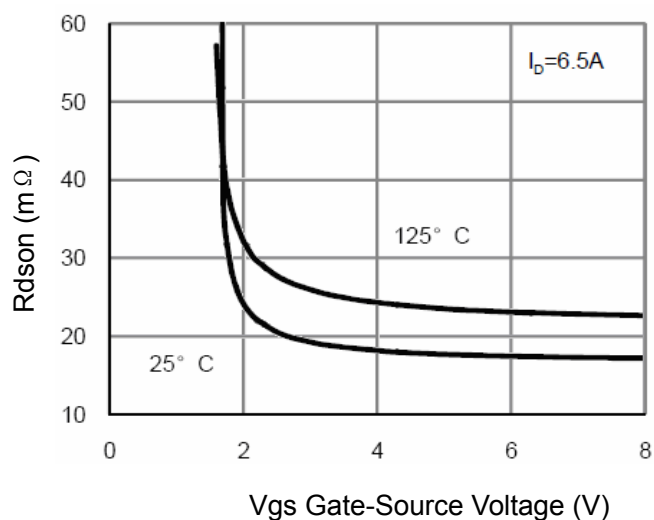


Figure 9 $R_{DS(on)}$ vs V_{GS}

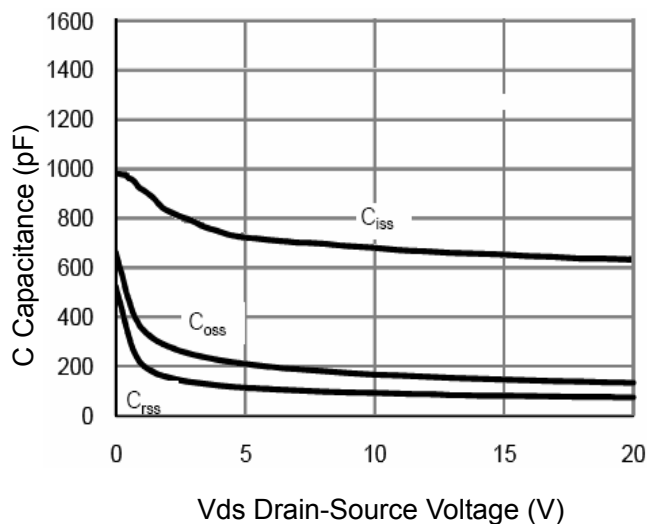


Figure 10 Capacitance vs V_{DS}

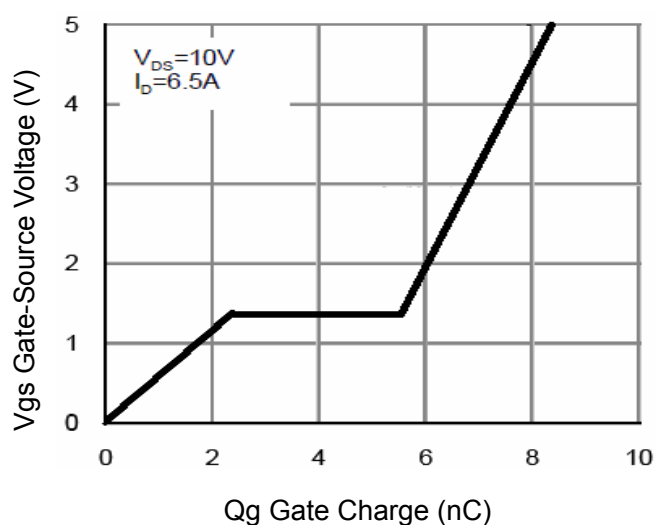


Figure 11 Gate Charge

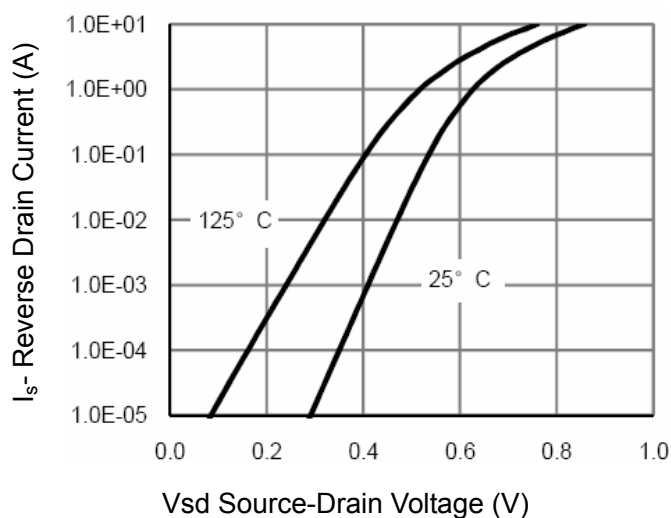
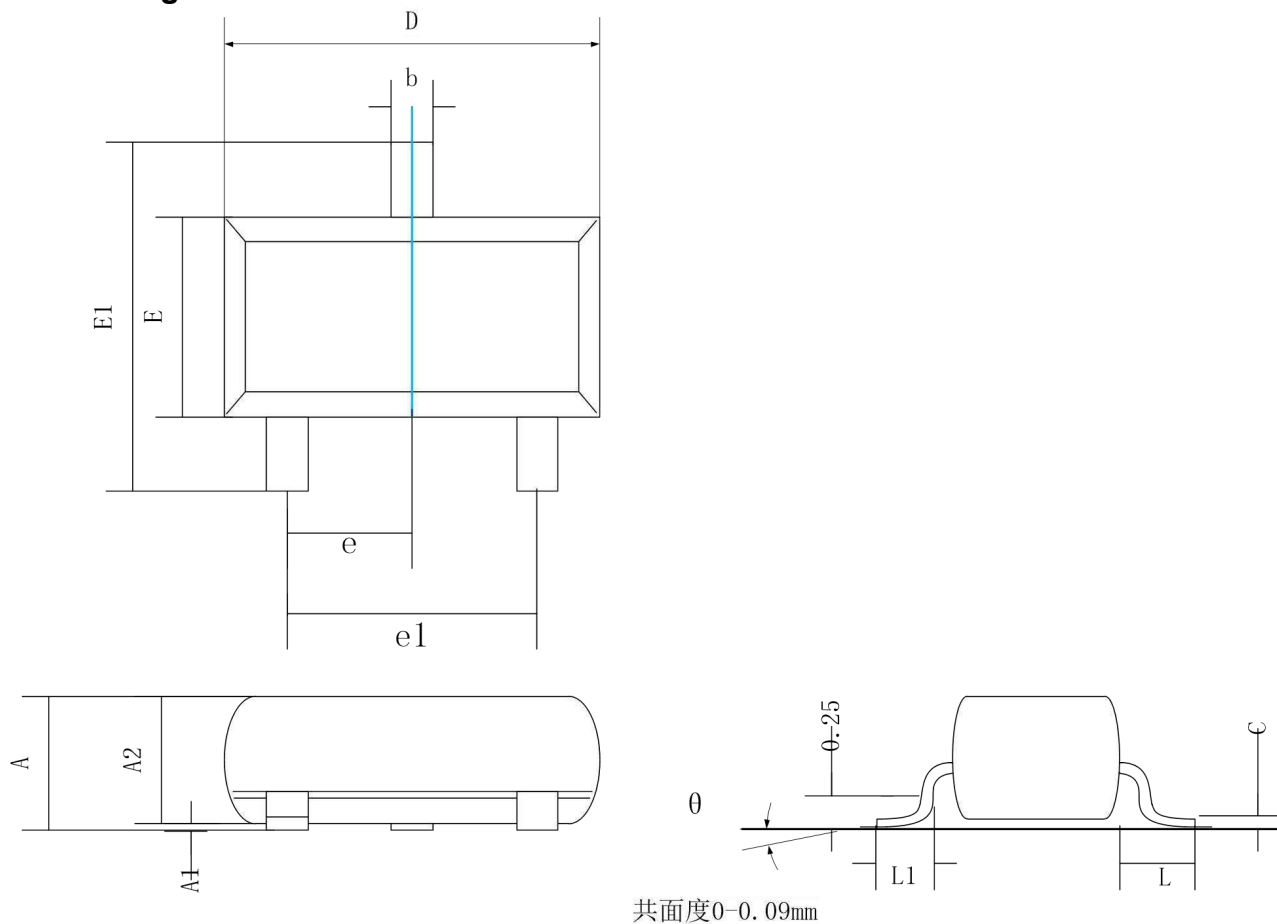


Figure 12 Source- Drain Diode Forward

SOT-23 Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	0.90	1.15
A1	0.00	0.10
A2	0.90	1.05
b	0.30	0.50
c	0.08	0.15
D	2.80	3.00
E	1.20	1.40
E1	2.25	2.55
e	0.95 REF.	
e1	1.80	2.00
L	0.55 REF.	
L1	0.30	0.50
θ	0°	8°

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